

An Architecture Description Language for Embedded Hardware Platforms

Guillaume Savaton
guillaume.savaton@eseo.fr
ESEO/TRAME, Angers, France

Jean-Luc Béchennec
jean-luc-bechennec@irclyn.ec-nantes.fr

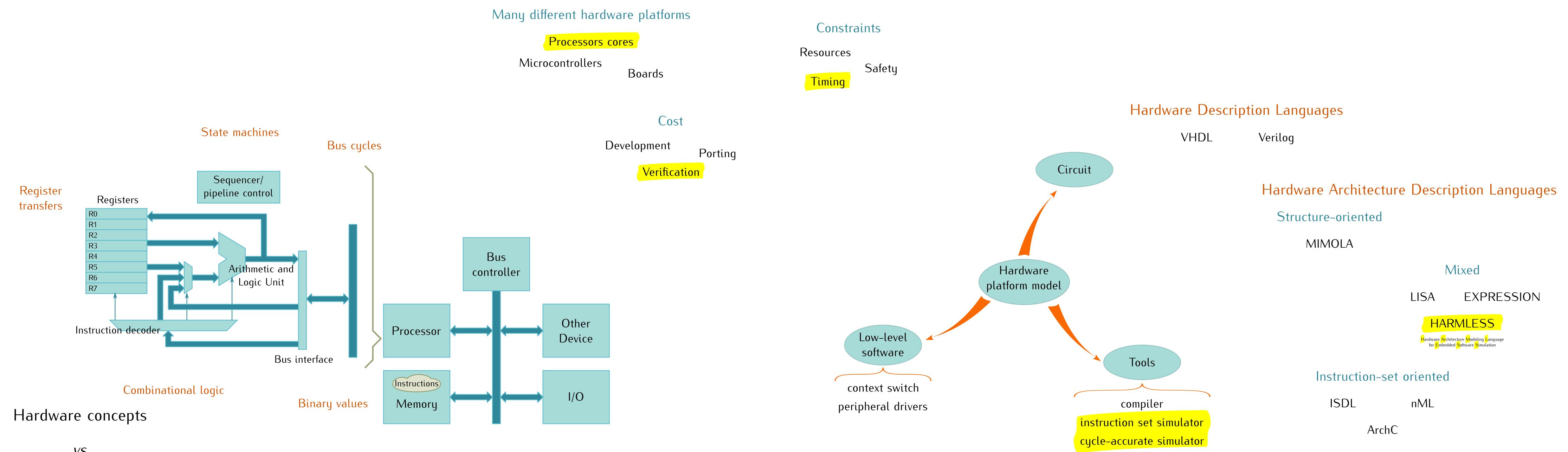
Mikaël Briday
mikael.briday@irclyn.ec-nantes.fr

Rola Kassem
r.kassem@bau.edu.lb

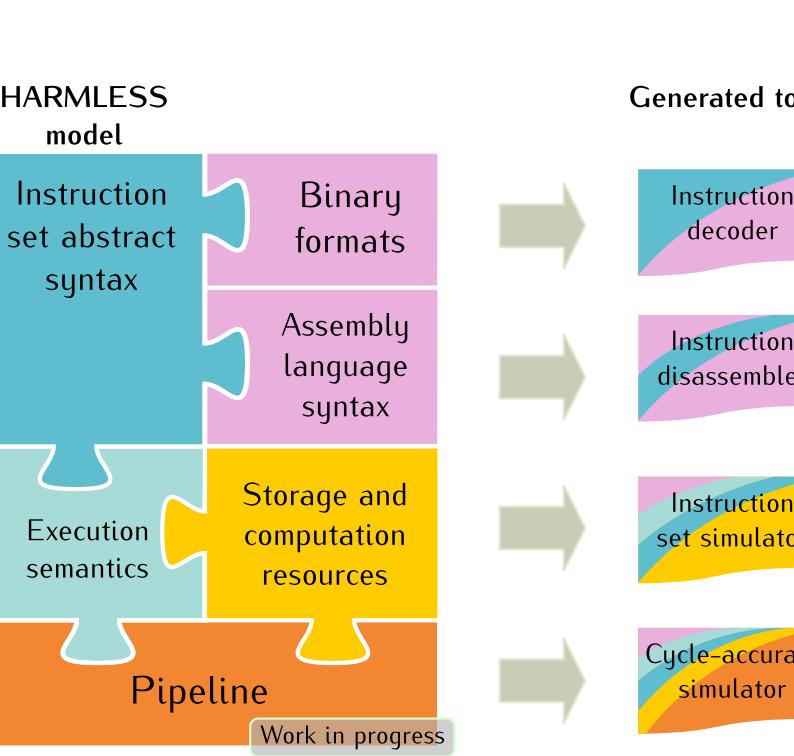
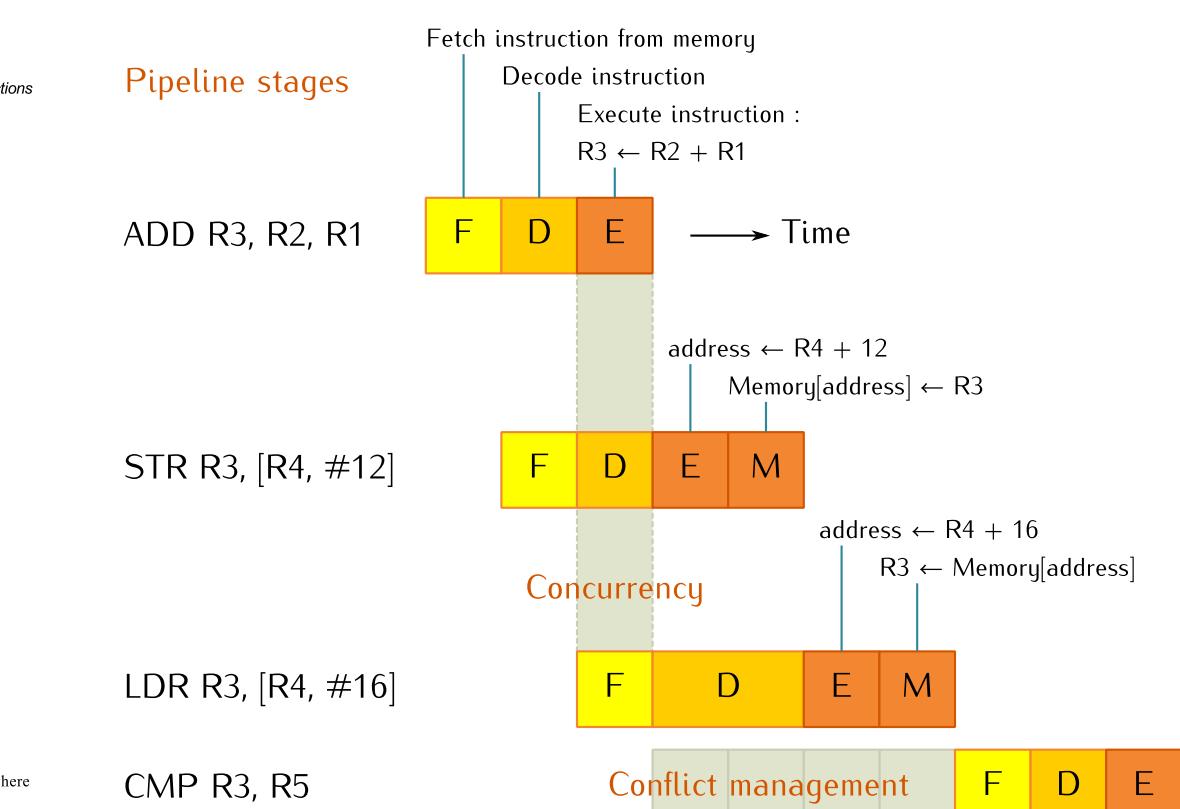
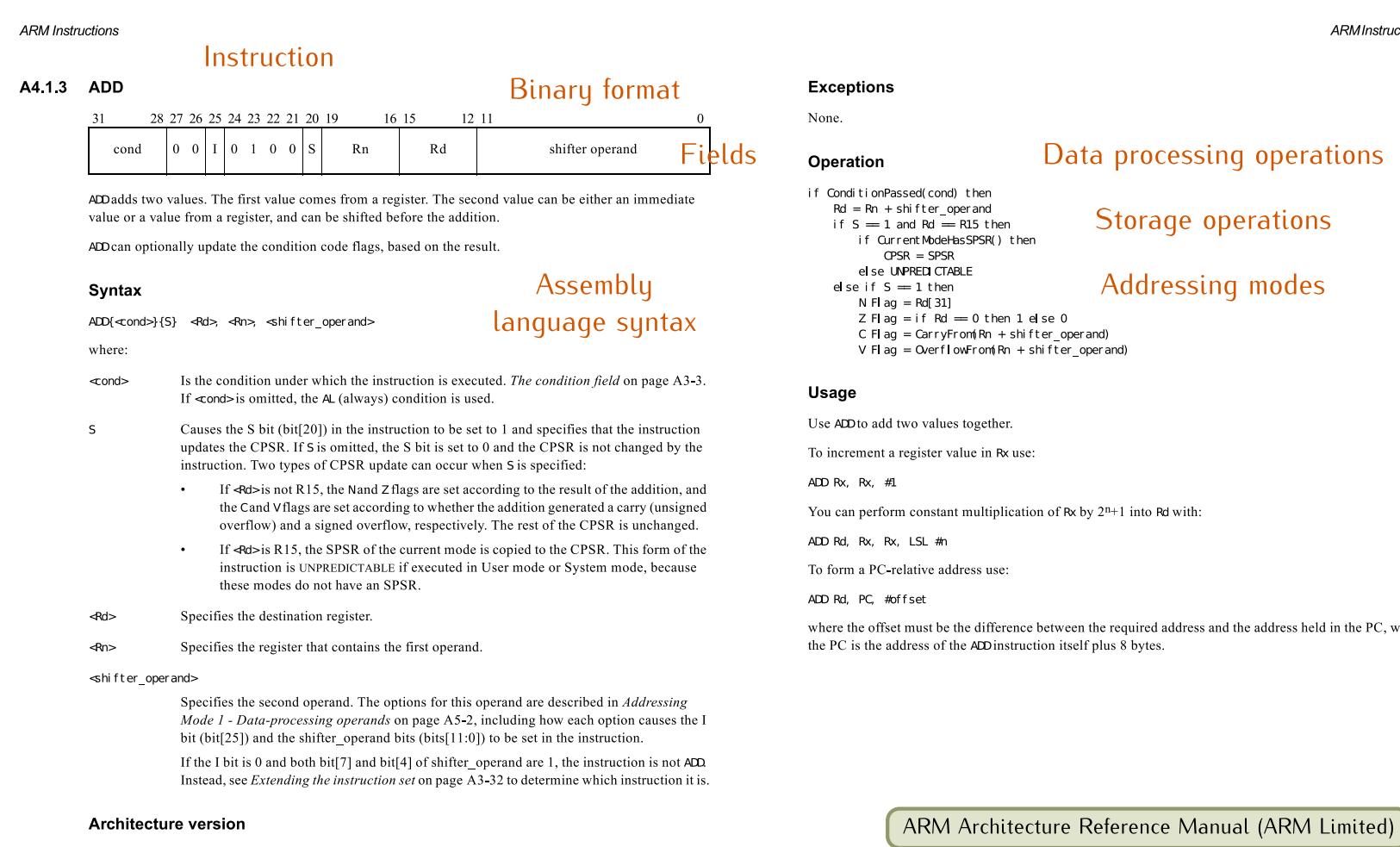
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Realtime Embedded Systems



Hardware platform concepts



Seems redundant, but a given partial instruction can have different formats depending on the context.

